

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	
Filing Date	November 8, 2001
Confirmation No	4689
Inventor	Keiji Jono et al.
Assignee	KMT Semiconductor, LTD
Group Art Unit	2811
Examiner	Thien F. Tran
Attorney's Docket No	KM1-003
Customer No	021567
Title: Trench-Isolated Transistors	, Trench Isolation Structures, Memory
Cells, and DRAMs	

RESPONSE TO MARCH 16, 2004 OFFICE ACTION

To:

Mail Stop Amendment

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

VIA U.S. EXPRESS MAIL

From:

Mark Matkin (Tel. 509-624-4276; Fax 509-838-3424)

Wells St. John P.S.

601 West First Avenue, Suite 1300

Spokane, WA 99201-3828

Responsive to the Office Action dated March 16, 2004, Applicant amends and remarks as follows:

<u>AMENDMENTS</u>